

EAST - [Untitled1:1]

File View Edit Tools Window Help

☐ Drafts  
☒ Pending  
☒ Active

L1: (118483) disk adj1 drive  
 L2: (33341) clock near5 delay  
 L3: (723546) recording  
 L4: (745) pre-compensate or pre-compensation  
 L5: (755) precompensate or precompensation  
 L6: (1391) 4 or 5  
 L7: (0) 1 same 2 same 3 same 6  
 L8: (65) 1 and 2 and 3 and 6

☐ Failed  
☒ Saved

DB: USFORUS/USPAT/EPD/PO/EN/IDR  
 Default operator: OR  
 1 and 2 and 3 and 6

☐ Print  
☐ Highlight all hits on entry

☒ B&B form ☒ IS&B form ☐ Image ☐ Text ☐ HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current ER	Retrieval	Inventor	S	C	P	J	
1	<input checked="" type="checkbox"/>	US 20030189778	20031009	14	Disk drive writer	360/29	360/46;		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input checked="" type="checkbox"/>	US 20020176299	20021128		waveform induced precom	365/221	360/66		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input checked="" type="checkbox"/>	US 20020176194	20021128		High-speed zero phase restart of a multiphase	360/51	365/233		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input checked="" type="checkbox"/>	US 20020176188	20021128		High-speed programmable synchronous counter for	360/46			Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input checked="" type="checkbox"/>	US 20020176186	20021128		Offset cancellation of charge pump based phase	360/32	360/51		Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input checked="" type="checkbox"/>	US 20020175724	20021128		Efficient analog front end for a read/write ch	327/172			Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input checked="" type="checkbox"/>	US 20020159176	20021031		Method and system for managing a pulse width	360/46	360/51		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input checked="" type="checkbox"/>	US 20020154430	20021024		Look ahead write precompensation	360/25			Rae, James W. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input checked="" type="checkbox"/>	US 20020126604	20020912		Read/write channel	369/47.53	369/59.11		Powelson, Judith C. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input checked="" type="checkbox"/>	US 20020015247	20020207		Write compensation for data storage and commun	360/45	360/68		ROSKY, DAVID S. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input checked="" type="checkbox"/>	US 6785071 B2	20040831		HIGH RESOLUTION WIDE RANGE WRITE PRECOMPENSA	360/29	360/46;		Elliott, Carl F. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input checked="" type="checkbox"/>	US 6738205 B1	20040518		Disk drive writer	360/17	360/66		Moran, Patrick et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
13	<input checked="" type="checkbox"/>	US 6700722 B2	20040302		Self-writing of servo patterns in disk drives	360/51	360/75		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
14	<input checked="" type="checkbox"/>	US 6690525 B2	20040210		High-speed zero phase restart of a multiphase	360/51	327/152;		Ruegg, Michael A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
15	<input checked="" type="checkbox"/>	US 6662303 B1	20031209		High-speed programmable synchronous counter for	713/400	331/25;		Toosky, Zabih et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
16	<input checked="" type="checkbox"/>	US 6661590 B2	20031209		Write precompensation circuit and read channel	360/32	711/167;		Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
17	<input checked="" type="checkbox"/>	US 6653879 B2	20031125		Efficient analog front end for a read/write ch	327/172	713/500;		Cyrusian, Sasan et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
					Method and system for managing a pulse width		360/66;			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
							327/173;			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
							327/174			<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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D S A W T * *											
Drafts											
#		Document ID	Issue Date	Pages	Title	Current OR	Current KR	Retrieval	Inventor	S	C
18	<input checked="" type="checkbox"/>	US 6621648	20030916		Look ahead write	360/45	360/46;		Elliott; Carl F.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B2			precompensation		360/51;		et al.	<input type="checkbox"/>	<input type="checkbox"/>
19	<input checked="" type="checkbox"/>	US 6594094	20030715		Read/write channel	360/25	360/46;		Rae; James W. et	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B2					360/51;		al.	<input type="checkbox"/>	<input type="checkbox"/>
20	<input checked="" type="checkbox"/>	US 6438703	20020820		Method and system for	713/401	713/503		Berning; Lynn	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			selectively varying sig				Charles et al.	<input type="checkbox"/>	<input type="checkbox"/>
21	<input checked="" type="checkbox"/>	US 6404572	20020611		Circuit and method for	360/45	360/51;		Hong; Ghy-Boong	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			generating a write prec		360/76			<input type="checkbox"/>	<input type="checkbox"/>
22	<input checked="" type="checkbox"/>	US 6337778	20020108		Disk drive employing	360/51	360/46;		Gagne;	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			vector addition of prim		360/68		Jean-Sebastien	<input type="checkbox"/>	<input type="checkbox"/>
23	<input checked="" type="checkbox"/>	US 6243031	20010605		Selectable delay	341/68	327/157		Jusuf; Gani et al.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			circuit having immunity					<input type="checkbox"/>	<input type="checkbox"/>
24	<input checked="" type="checkbox"/>	US 6219192	20010417		Method and apparatus	360/25	360/46;		Gopalaswamy;	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			for the compensation of		360/53;		Srinivasan et al.	<input type="checkbox"/>	<input type="checkbox"/>
25	<input checked="" type="checkbox"/>	US 6208184	20010327		Method and device for	327/161	327/263		Demicheli; Marco	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			delaying selected trans				et al.	<input type="checkbox"/>	<input type="checkbox"/>
26	<input checked="" type="checkbox"/>	US 6191906	20010220		Method for estimating	360/51	360/46		Buch; Bruce D.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	B1			sampling phase from svn					<input type="checkbox"/>	<input type="checkbox"/>
27	<input checked="" type="checkbox"/>	US 6133861	20001017		Selectable delay	341/68	327/157		Jusuf; Gani et al.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			circuit having immunity					<input type="checkbox"/>	<input type="checkbox"/>
28	<input checked="" type="checkbox"/>	US 6108153	20000822		Servo demodulator and	360/51	360/77.08		Glover; Kerry C.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			method for synchronous					<input type="checkbox"/>	<input type="checkbox"/>
29	<input checked="" type="checkbox"/>	US 6043944	20000328		Method and apparatus	360/51			Yamasaki; Richard	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			for fail-safing and exte				G. et al.	<input type="checkbox"/>	<input type="checkbox"/>
30	<input checked="" type="checkbox"/>	US 6009534	19991228		Fractional phase	713/503	331/107R		Chiu; Kar-Shing et	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			interpolation of ring o				al.	<input type="checkbox"/>	<input type="checkbox"/>
31	<input checked="" type="checkbox"/>	US 5986830	19991116		Read/write channel	360/45	360/51		Hein; Jerrell P.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			write precompensation s					<input type="checkbox"/>	<input type="checkbox"/>
32	<input checked="" type="checkbox"/>	US 5808822	19980915		Apparatus and method	360/65	360/51		Kurachi; Koji et	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			for correcting a read s				al.	<input type="checkbox"/>	<input type="checkbox"/>
33	<input checked="" type="checkbox"/>	US 5734680	19980331		Analog implementation	375/263	375/262;		Moore; Charles E.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			of a partial response m		375/341;		et al.	<input type="checkbox"/>	<input type="checkbox"/>
34	<input checked="" type="checkbox"/>	US 5598364	19970128		All-MOS precision	365/73	360/51;		McCall; Kevin J.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			differential delay line		365/189.05		et al.	<input type="checkbox"/>	<input type="checkbox"/>
35	<input checked="" type="checkbox"/>	US 5592515	19970107		Fully digital data	375/340	327/156;		Saban; Rami et al.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			separator and frequency		327/160;			<input type="checkbox"/>	<input type="checkbox"/>
36	<input checked="" type="checkbox"/>	US 5583705	19961210		Write precompensation	360/45	360/46		Ziperovich; Pablo	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			optimization in a PAML				A. et al.	<input type="checkbox"/>	<input type="checkbox"/>
37	<input checked="" type="checkbox"/>	US 5559645	19960924		Disk recording	360/51	360/53		Miyazawa; Shyoichi	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			apparatus with adaptive				et al.	<input type="checkbox"/>	<input type="checkbox"/>
38	<input checked="" type="checkbox"/>	US 5553100	19960903		Fully digital data	375/340	327/156;		Saban; Rami et al.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			separator and frequency		327/160;			<input type="checkbox"/>	<input type="checkbox"/>
39	<input checked="" type="checkbox"/>	US 5552942	19960903		Zero phase start	360/51	375/369;		Ziperovich; Pablo	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			optimization using mean		375/376		A. et al.	<input type="checkbox"/>	<input type="checkbox"/>
40	<input checked="" type="checkbox"/>	US 5543975	19960806		Removal of	360/45	360/46		Curran; Daniel J.	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			precompensation in a w					<input type="checkbox"/>	<input type="checkbox"/>
41	<input checked="" type="checkbox"/>	US 5517146	19960514		Write precompensation	327/276	327/261;		Yamasaki; Richard	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A			circuit for compensatin		360/46			<input type="checkbox"/>	<input type="checkbox"/>
42	<input checked="" type="checkbox"/>	US 5493454	19960220		Write precompensation	360/45	360/46;		Ziperovich; Pablo	<input type="checkbox"/>	<input type="checkbox"/>
	<input type="checkbox"/>	A								<input type="checkbox"/>	<input type="checkbox"/>

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U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XR	Retrieval	Inventor	S C P 3
42	<input checked="" type="checkbox"/>	US 5493454	19960220		Write precompensation optimization in a PRML	360/45	360/46;		Ziperovich; Pablo A. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
43	<input checked="" type="checkbox"/>	US 5459757	19951017		Timing and gain control circuit for a PRML read	375/376	360/53;		Minuhin; Vadim B. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
44	<input checked="" type="checkbox"/>	US 5422760	19950606		Disk drive method using zoned data recording and	360/46	360/51;		Abbott; William L. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
45	<input checked="" type="checkbox"/>	US 5402274	19950328		Data separator and signal processing circuit	360/51	360/65;		Miyazawa; Shyoichi et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
46	<input checked="" type="checkbox"/>	US 5381359	19950110		Adaptation and training of digital finite impulse	708/322	360/40		Abbott; William L. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
47	<input checked="" type="checkbox"/>	US 5375145	19941220		Multi-mode gain control loop for PRML class IV	375/345	455/234.1		Abbott; William L. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
48	<input checked="" type="checkbox"/>	US 5369376	19941129		Programmable phase locked loop circuit and	331/8	331/16;		Leblebicioglu; Hakan	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
49	<input checked="" type="checkbox"/>	US 5345342	19940906		Disk drive using PRML synchronous sampling data	360/48	331/17;		Abbott; William L. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
50	<input checked="" type="checkbox"/>	US 5341387	19940823		Viterbi detector having adjustable detection threshold	714/788	360/77.08		Nguyen; Hung C.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
51	<input checked="" type="checkbox"/>	US 5341249	19940823		Disk drive using PRML class IV sampling data	360/46	375/341;		Abbott; William L. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
52	<input checked="" type="checkbox"/>	US 5331479	19940719		Capacitor assisted low voltage write circuit	360/68	375/348;		Madsen; Timothy A.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
53	<input checked="" type="checkbox"/>	US 5321559	19940614		Asynchronous peak detection of information	360/46	360/46;		Nguyen; Hung C. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
54	<input checked="" type="checkbox"/>	US 5278702	19940111		Data synchronizer with symmetric window generation	360/51	360/65;		Wilson; Ronald E. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
55	<input checked="" type="checkbox"/>	US 5258933	19931102		Timing control for PRML class IV sampling data	708/3	375/376;		Johnson; Kenneth E. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
56	<input checked="" type="checkbox"/>	US 5231545	19930727		Fault tolerant RLL data sector address mark detection	360/49	360/46;		Gold; Clifford M.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
57	<input checked="" type="checkbox"/>	US 5212601	19930518		Disk drive data synchronizer with window	360/51	341/59;		Wilson; Ronald E. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
58	<input checked="" type="checkbox"/>	US 5187615	19930216		Data separator and signal processing circuit	360/46	360/40;		Miyazawa; Shyoichi et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
59	<input checked="" type="checkbox"/>	US 5025327	19910618		Magnetic disk write precompensation method	360/45	360/27		Stamness; Jesse I. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
60	<input checked="" type="checkbox"/>	US 4989219	19910129		Midlevel carrier modulation and demodulation	375/286	329/363;		Gerdes; Richard C. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
61	<input checked="" type="checkbox"/>	US 4964107	19901016		Relative timing precompensation of high	368/120	332/115;		Galbraith; Richard L. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
62	<input checked="" type="checkbox"/>	US 4878028	19891031		Technique for generating precompensation	327/278	360/51		Wang; Yun-Che et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
63	<input checked="" type="checkbox"/>	US 4809088	19890228		Integrated system for implementation of read	360/31	327/101;		Loifgren; Karl M. J. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
64	<input checked="" type="checkbox"/>	US 4544964	19851001		Strobe for read/write chain	360/51	360/45;		Burns; Kenneth R. et al.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
65	<input checked="" type="checkbox"/>	US 4281356	19810728		Magnetic disk memory	360/45	260/51		Sousa; Frank E.	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>

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